

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1148969
<b>Application Number:</b>	10759564
<b>Confirmation Number:</b>	8131
<b>Title of Invention:</b>	Microprocessor and apparatus for performing speculative load operation from a stack memory cache
<b>First Named Inventor:</b>	Rodney E. Hooker
<b>Customer Number:</b>	23669
<b>Filer:</b>	James Weldon Huffman/Taysie Locke
<b>Filer Authorized By:</b>	James Weldon Huffman
<b>Attorney Docket Number:</b>	CNTR.2229
<b>Receipt Date:</b>	10-AUG-2006
<b>Filing Date:</b>	16-JAN-2004
<b>Time Stamp:</b>	12:26:04
<b>Application Type:</b>	Utility
<b>International Application Number:</b>	

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	CNTR_2229_IDS.pdf	334518	no	3

**Warnings:****Information:**

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**Total Files Size (in bytes):**

334518

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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.